

True Single Supply:

True Single Supply D/A's, Usually with on-board output amplifier.																		R	Model Designator								
		Power Supply Requirements		Is in	Output Voltage		Output Current	Settling Time usec	Accuracy or Linearity	Differential Linearity	Full Scale Error	Zero Error	Voltage Reference	I/O	# of Buffers	C L	B A	Temperature Range				# of Pins	Price				
#	BITS	D/A's	+Vs + Volts	+ Is mA	sdown uA	+5Vdd	+15Vdd	mA	+5V	+15V	+25C Tmax	+25C Tmax	+25C Tmax	+25C Tmax	Int	Ext	R K	+70C 85	85	125	100's						
SINGLES																											
AD557	7	1	+5V	25		+2.56V		5	1.5		1	1	1	1	2.5	4	1	5	1.2V	P8	1	N	N	J		16 \$5.85	
AD7801	8	1	+3V	1.5	1	0 to V ref		1	2.5		1	1	1	1	2	2	2	2	+Vref	P8	2	Y	N		A	20 TBD	
AD558	8	1	+5V	25		+2.56V	+10V	5	1.5	5	1/2	1/2			1.5	2.5	1	2	1.2V	P8	1	N	N	J		S 16 \$6.82	
AD558	8	1	or +15	25		+2.56V	+10V	5	1.5	5	1/4	1/4			1/2	1	1/2	1	1.2V	P8				K	T	\$10.54	
AD5300	8	1	+2.7V	0.15	0.1	0 to Vdd		5	5		1	1	1	1	5	5	5	5	Vdd	S3	1	Y	N		A	6/8	
AD7224	8	1	+15V	6			+10V	5		2								+Vref			N	N	K	B	T	18 \$4.95	
AD8402 Digital Pot																											
AD8402	8	2	+5V	4mA	10	0>VDD		1/10/50/100K	100	23	2	2	1	1	4	4	2	2	S 10MH		2	Y	N		A	14 \$1.95	
AD7302	8	2	+3V	2.5	10	0>Vdd	5	5	1	1	1	1	8	8	2	2	na	na	+Vref	P8	2	Y	N	J	A S	8 \$2.95	
AD7303	8	2	+3V	2.5	10	0>Vdd	5	5	1	1	1	1	8	8	2	2	na	na	+Vref	S	2	Y	N	J	A S	20 \$3.45	
AD5310	10	1	+2.7V	0.15	0.1	0 to Vdd		5	5		1	1	1/2	1/2	5	5	5	5	Vdd	S3	1	Y	N		A	6/8	
AD7391	10	1	+3V	0.1	1	0 to Vdd	+1,-3	70		1.75	2	0.9	1	32mV	35mV	9mV	9mV	+Vref	SPI	2	Y			A	8 \$3.29		
AD7393	10	1	+3V	0.1	1	0 to Vdd	+1,-3	60		1.75	2	0.8	0.8	32mV	32mV	9mV	9mV	+Vref	P10	1	Y	N		A AR	20 \$3.47		
AD9760	10	1	+3/5V	35	8	-1V>+1V	na	2/20	35		2.5	4	1	1	100	100	2.5	2.5	+1.2V	P10	1	N	N		A	28 \$8.50	
AD5320	12	1	+2.7V	0.15	0.1	0 to Vdd		5	5		4	4	1/2	1/2	8	8	8	8	Vdd	S3	1	Y	N		A	6/8	
AD7243	12	1	12	10			5		10	4	4	0.9	0.9	6	6	4	4	+5V		S3Mhz	2	Y	Y		A	16 \$7.00	
AD7243	12	1	or			or				1/2	1/2													B		\$8.50	
AD7243	12	1	+15V	12			12		12	1	4			7	7	5	5								S		
AD9762	12	1	+3/5V	35	8	-1V>+1V	na	2/20	35		2.5	4	1.5	2	400	400	10	10	+1.2V	P12	1	N	N		A	28 \$10.00	
AD7390	12	1	+3V	0.1		0 to Vdd	+1,-3	70		1.6	2	0.9	1	8mV	20mV	4	4	+Vref	SPI	2	Y			A	8 \$5.60		
AD7392	12	1	+3V	0.1	0.1	0 to Vdd	+1,-3	60		1.8	3	0.9	1	8mV	20mV	9mV	8mV	+Vref	P10	1	Y	N		A AR	20 \$5.95		
AD8300	12	1	+5V	1.7		2.0473		5	14		2	2	1	1	4	1	6	6	2.0475		S16Mhz	2	Y	N		A	8 \$5.25
AD8300	12	1	+5V	1.7					15																		
DAC8512	12	1	+5V	2.3/TTL		4.096	N/A	±3	16		2	2	1	1	4	1	6	6	2.0475		S14.2MHz	1	Y	N		F	8 \$5.35
DAC8512										1	1			1	1									E		\$10.95	
DAC8562	12	1	+5V	6/TTL		4.096	N/A	±5	15		1	1	1	1	15	15	5	5	+2.5V	P12	1	Y	N		F	20 \$8.95	
DAC8562						1/CMOS				1/2	1/2			7	7									E		\$12.50	
DUALS																											
AD7395	10	1	+3V	0.2	1	0 to Vdd		2	60		1	1	1/2	1/2	TBD	TBD	TBD	TBD	+Vref	S	2				A	14 \$6.84	
AD7397	10	1	+3V	0.2	1	0 to Vdd		2	60		1	1	1/2	1/2	TBD	TBD	TBD	TBD	+Vref	P10	2				A	24 \$7.07	
AD7237	12	2	+15V	18		+10V	5		5	1	1	0.9	0.9	5	5	5	5	+5V	P8	2	N	N		J/A S	24 \$16.50		
AD7237	12	2								1/2	1/2													K/B		\$19.36	
AD7237	12	2								1	1	0.9	0.9	8	8	5	5							T			
AD7237A	12	2	+12	18		+10V	5		5	1	1	0.9	0.9	5	5	5	5	+5V	P8	2	N	N		A	24 \$16.50		
AD7237A	12	2								1/2	1/2													B		\$19.36	
AD7237A	12	2								1	1	0.9	0.9	6	6	4	4							T			

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		Power Supply Requirements		I _s in	Output Voltage		Output Current	Settling Time usec	Accuracy or Linearity	Differential Linearity	Full Scale Error	Zero Error	Voltage Reference	I/O	# of Buffers	C L	B A	Temperature Range			# of Pins	Price		
MODEL	BITS	# D/A's	# +Vs + Volts	+ I _s mA	sdown uA	+5Vdd	+15Vdd	mA	+5V +15V	+25C Tmax	+25C Tmax	+25C Tmax	+25C Tmax	Int	Ext	A R	C K	0 +70C	-25 85	-40 85	-55 125	100's		
AD7247	12	2	+15V	18		+10V	5		5	1 1/2	1 1/2	0.9 0.9	5 5	5 5	+5V	P12	2	N N	J/A	S	24	\$16.50		
AD7247	12	2																	K/B			\$19.36		
AD7247	12	2							1	1	0.9	0.9	8	8	5	5				T				
AD7247A	12	2	+12	18		+10V	5		5	1 1/2	1 1/2	0.9 0.9	5 5	5 5	+5V	P12	2	N N	A		24	\$16.50		
AD7247A	12	2																	B			\$19.36		
AD7247A	12	2							1	1	0.9	0.9	6	6	4	4				T				
AD7249	12	2	+12V or +15V			+5/10V	5		5	1 1/2	1 1/2	0.9 0.9	5 5	NS NS	5 3	S2Mhz	1 Y	N J	A	S	24	\$10.85		
AD7249						±5V												K	B			\$15.55		
AD7394	12	1	+3V	0.2	1	0 to Vdd		2	75		1 1	1/2 1/2	TBD TBD TBD TBD	+Vref	S	2			A		14	\$9.14		
AD7396	12	1	+3V	0.2	1	0 to Vdd		2	75		1 1	1/2 1/2	TBD TBD TBD TBD	+Vref	P12	2			A		24	\$9.26		
AD8303	12	2	+5V	2	50	2.0473	+5/-5	14		2 2	1/2 1	17 17	6 6	2.0473	S16Mhz	2 Y	N	A		8	\$10.00			
AD8303	12	2	+5V	1.7				15																
AD8322	12	2	+5V	3/TTL		4.096	N/A	±5	15		2 2	1 1			+2.3V	S14.2Mhz	1 Y	Y	A		14	\$9.95		
AD8382	12	2	+5V	7/TTL		4.096	N/A	±5	15		2 2	1 1	15 15	3 3	+2.3V	P12	1 Y		A		24	\$11.95		
AD8382				2/CMOS																				
QUAD	QUAD					QUAD		QUAD						QUAD	QUAD			QUAD	QUAD		QUAD			
AD7304	8	4	+3V	5	1	0 to Vdd		1	2 to 1/2FS		1 1	1 ±10	10 10	4 @ +Vref	S12Mhz	2 Y	N	A		16	\$5.75			
AD7305	8	4	+3V	5	1	0 to Vdd		1	2 to 1/2FS		1 1	1 ±10	10 10	+Vref	P8	2	N N	A		20	\$6.15			
AD8403 Digital Pot																								
AD8403	8	4	+5V	4mA	10	0>VDD	1/10/50/100K	100	23	2 2	1 1	4 4	2 2		S 10MH	2 Y	N	A		14	\$2.95			
AD7225	8	4	+15V	10		+10V	5		5		1 1	2 2	NS NS	4 @ +Vref	P8	2 N	N K B	U		24	\$18.50			
AD7226	8	4	+15V	15		+10V	5		5		1 1	2 2	NS NS	+10V	P8	2 N	N K B	U		20	\$18.50			
PM7226	8	4												(Total unadjusted error=1 LSB)				F	B		\$14.80			
PM7226	8	4												(Total unadjusted error=1/2 LSB)				N	G	E	A	20	\$16.65	
AD7804 and AD7805 have 8 bit SUB D/A's per channel for adjusting Vbias																								
AD7804	10	4	+5.5V	12	250	Vbias +/-Vswing		2	4		3 3			33mV 33mV 33mV 33mV	+1.2V		S10Mhz	2 Y	N	B		16/28	\$5.75	
AD7804	10	4	+5.5V	12	250	Vbias +/-Vswing		2	4		2 2							C				\$6.25		
AD7805	10	8	+5.5V	12	250	Vbias +/-Vswing		2	4		3 3			33mV 33mV 33mV 33mV	+1.2V		P8/12	2 Y	N	B		16/28	\$6.25	
AD7805	10	8	+5.5V	12	250	Vbias +/-Vswing		2	4		2 2							C				\$6.95		
DAC8420	12	4	+5V			0 to Vs-2.5V		±1.25			4 4	1 1	8 8	8 8	1 @ Vh	S	2 Y	N	F			\$29.95		
DAC8420	12	4									2 2				1 @ VI			E				\$34.95		
AD7398	12	4	5	0.8	100	0>Vdd		5	16	1 1	1 1				4 @ +Vref	SPI	2 N	Y	B		16	\$13.50		
OCTAL	OCTAL					OCTAL		OCTAL						OCTAL	OCTAL			OCTAL	OCTAL					
AD7317	6	8	+5V or 12V	3		0 to Vdd		1	6		1 1	1/2 1/2	1/2 1/2	1 1	1 @ Vh	I2C	1 N	N	A		16			
AD7318	6	8	+5V or 12V	3		0 to Vdd		1	6		1 1	1/2 1/2	1/2 1/2	1 1	1 @ Vh	I2C	1 N	N	A		16			

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True Single Supply D/A's, Usually with on-board output amplifier.																			R	Model Designator									
		Power Supply		Is	Output		Output	Settling Time		Accuracy	Differential		Full Scale		Zero			# of	C	L	B	Temperature							
		Requirements		in	Voltage	Current	usec	or Linearity		Linearity	Error		Error		Voltage		I/O	Input	E	A	Range				#	Price			
MODEL	BITS	#	#	+Vs	+ Is	sdown		to 1/2 LSB		Lsb's	Lsb's		Lsb's		Lsb's		Reference		Buffers	A	C	0	-25	-40	-55	of			
		D/A's	+ Volts	+ mA	+ uA	+5Vdd	+15Vdd	mA	+5V	+15V	+25C	Tmax	+25C	Tmax	+25C	Tmax	+25C	Tmax	Int	Ext	R	K	+70C	85	85	125	Pins	100's	
AD7228A	8	8	+15V	16		+10V	5		5	2	2	1	1	2	2	2	2		+10V	P8	1	N	N		B	T	24	\$17.25	
AD7228A									1	1	1	1	1	1	1	1	1								C	U		\$17.25	
AD7228A	8	8	+5V	14		1.25	0.5		NS	2	2	1	1	2	2	2	2		1.23	P8	1	N	N		B	T	24	\$17.50	
AD7228A									1	1	1	1	1	1	1	1	1								C	U		\$20.25	
DAC8841	8	8	+5V	26		+1.5V		5	6		1.5	1.5	1	1			4	4		8 @ +Vref \$6.2Mhz		Y	Y		F		24	\$9.95	
<i>AD7808 and AD7809 have 8 bit SUB D/A's per channel for adjusting Vbias</i>																													
AD7808	10	8	+5.5V	8		Vbias-Vswing		2	2		1	1	1	1	25mV>>>>>>>>>		+1.2V		S10Mhz	2	Y	N		A		20/44	\$10.00		
AD7809	10	8	+5V	8		Vbias-Vswing		2	2		1	1	1	1	25mV>>>>>>>>>		+1.2V		p12	2	Y	N		A		20/44	\$10.00		
TRIM DACS, NO OUTPUT AMPLIFIER, VOLTAGE SWITCHING R/2R																													
AD8801 Resets to Mid Scale																													
AD8801	8	8	+3 or +5V	4		0 to Vdd		16K ohm		1 1/5	1 1/5	1	1	-4,+.5	-4,+.5	1/2	1/2		1 @ Vh	\$30Mhz	1	Y	N		A		16	\$4.15	
AD8803	8	8	+3 or +5V	4		0 to Vdd		16K ohm		1 1/5	1 1/5	1	1	-4,+.3	-4,+.3	1/2	1/2		1 Vh & 1Vlow	\$30Mhz	1	Y	N		A		16	\$4.15	
GREATER THAN OCTAL																													
AD8802 Resets to Mid Scale																													
AD8802	8	12	+5 or +5V	4		0 to Vdd		8K ohm	1	1 1/3	1 1/3	1	1	1	1	1	1		1 @ Vh	Z SER	1	N	N		A		20	tbd	
AD8804	8	12	+5 or +5V	4		0 to Vdd		8K ohm	1	1 1/3	1 1/3	1	1	1	1	1	1		1 Vh & 1Vlow	Z SER	1	N	N		A		20	tbd	
AD8600	8	16	+5V	32		0>+2.3V		2	2		1	1	1	1	1	1	3.3	3.3		+2.3V		2	Y	Y		A		44	\$12.05